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Arm Corelink Mmu 500 System

Arm Ltd. (stylized as arm) is a British semiconductor and software design company based in Cambridge, England. Its primary business is in the design of: ARM processors (CPUs), although it also designs other chips; software development tools under the DS-5, RealView and Keil brands; and systems and platforms, system-on-a-chip (SoC) infrastructure and software.

Arm Ltd. - Wikipedia

System Controllers: CoreLink GIC-400, CoreLink GIC-500, PL192

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VIC, BP141 TrustZone Memory Wrapper, CoreLink TZC-400, CoreLink L2C-310, CoreLink MMU-500, BP140 Memory Interface Security IP: CryptoCell-312, CryptoCell-712, TrustZone True Random Number Generator

ARM architecture - Wikipedia

Les architectures ARM sont des architectures externes de type RISC 32 bits (ARMv1 à ARMv7) et 64 bits [1] développées par ARM Ltd depuis 1983 et introduites à partir de 1990 par Acorn Computers. L'architecture ARM est le fruit du travail de Sophie Wilson.. Dotés d'une architecture relativement plus simple que d'autres familles de processeurs, et bénéficiant d'une faible consommation ...

Architecture ARM — Wikipédia

- ARM Platform Bus Type(CCI): CoreLink Cache Coherent Interconnect (CCI) is ARM BUS which provides full cache

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coherency between two clusters of multi-core CPUs and I/O

coherency for devices and I/O masters

CONFIG_SYS_FSL_HAS_CCI400 Defined For SoC that has cache

coherent interconnect CCN-400 CONFIG_SYS_FSL_HAS_CCN504

Defined for SoC that has ...

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